

PRODUCT OVERVIEW

SA3625 is an intelligent eMMC5.1 controller especially tailored for embedded-NAND oriented application with interface speed support up to 533MB/s (HS533); not only fully complies with JEDEC eMMC 5.1 spec, but also backward compatible to legacy ones (eMMC4.3 ~ eMMC5.01). High-performance and low-power are achieved by adopting a powerful 32-bit CPU, patented architecture, proprietary hardwired ECC engine, dynamic power management and advanced process technology, making it suitable for portable applications like smart phones, tablets, e-books, GPS, gaming consoles and other embedded systems.

KEY FEATURES

- Support 1x/1y/1z nm, 3D MLC/TLC flash
- Support 3.3V/1.8V Flash
- Support SDR/ONFi 3.x/Toggle 2.0 interface
- Single channel with 8 chip selection
- Support maximum of 8-way interleave operation per channel
- Support 4K/8K/16K page size
- Support 1/2/4 plane flash
- Built-in configurable ECC per 1KB (max. 76-bit)
- Global Wear Leveling (erase count base)
- Support Program Error Recovery



BLOCK DIAGRAM

